**Joseph Peters, WORK LOG**

**MILESTONE 1 WORK:**

Tuesday, January 14, 2020

Met with team [4 hr]

We decided on the architecture and planned out most of the components there-in. For example we decided on what registers were used for what, what instruction types to use, and how to implement the necessary instructions at the machine code and hardware levels.

My tasks for milestone 2 were to be decided at the next group meeting.

**MILESTONE 2 WORK:**

Tuesday, January 21, 2020

Met with team [2 hr]

We worked on implementing the RTL and started planning for the data path elements.

Wednesday, January 22, 2020

Worked on procedure call example and spec [1 hr]

My tasks for milestone 3 were to brainstorm for data path specifications and start writing part tests for datapath components.

**MILESTONE 3 WORK:**

Tuesday, January 28, 2020

Met with team [3 hr]

We worked on implementing the datapath for our processor. We also talked about which parts would need to be made and which people would make each part.

Wednesday, January 29, 2020

Worked on the alu for the lab due Friday. Also started working on the main alu [4 hr]

My tasks for milestone 4 were to finish the alu lab and create the alu for our processor along with the test cases.

**MILESTONE 4 WORK:**

Friday, February 3, 2020

Finished the alu lab and planned the 16b alu [3 hrs]

Tuesday, February 7, 2020

Worked on 16b alu [2 hrs]

Wednesday, February 8, 2020

Finished 16b alu with changes for our implementation [2 hrs]

My tasks for milestone 4 were to finish the test cases for the alu and to assist in piecing the datapath together.